

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. - 21. (canceled).

22. (previously presented): A multi-supply-voltage semiconductor device comprising:

at least one block receiving a variable supply voltage, said at least one block receiving a clock signal;

a voltage level detector circuit which detects a voltage level of the variable supply voltage; and

at least one variable delay circuit which provides a delay in the clock signal received by the at least one block, said delay changing in accordance with a change in the voltage level detected by the voltage level detector circuit;

wherein said voltage level detector circuit comprises a differential amplifier into which the variable supply voltage and a reference voltage is input.

23-35. (canceled).